



THIS COPY IS PROVIDED ON A RESTRICTED BASIS AND IS NOT TO BE USED IN ANY WAY DETRIMENTAL TO THE INTERESTS OF PANDUIT CORP.

CHANNEL	INPUT PORT	OUTPUT PORT (50% OR 70% OF POWER)	TAP PORT (50% OR 30% OF POWER)	
1	I 1	02	Т1	
2	12	01	T2	
3	13	04	ТЗ	
4	I 4	03	T4	

В

								TITLE		
A								HDFE TAP MODULE, 2 PORT, LC-LC	A	
								CUSTOMER DRAWING		
								17F187AC/00 DATASET FILE NAME 1.7F187AC DC (00.00		
								17F187AC_DC/00A		
								UNLESS OTHERWISE SPECIFIED, DIMENSIONAL TOLERANCES ARE: IN [mm]		
								.x ± .xxx ± .010 [0.3] FHCT-*ZUC .xx ± .03 [0.8] ANGLES ±		
						SEE SHEET 1 FOR REVISION HISTORY		THIRD ANGLE PROJECTION 17F187AC-DC		
	REV	DATE	BY	CHK	APR	DESCRIPTION	ECN	DRAWN BY DATE CHK SCALE SAT 05-02-18 BONS NONE SHT 2 OF 2		
2					2		1			